

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Rich Fogal, Tracy Reynolds,
Timothy Cowles

Serial No.: Unknown

Filed: Concurrently Herewith (August 30, 2001)

For: METHOD AND APPARATUS FOR CIRCUIT
COMPLETION THROUGH THE USE OF BALL
BONDS OR OTHER CONNECTIONS DURING THE
FORMATION OF A SEMICONDUCTOR DEVICE

§ Group Art Unit: Unknown

§ Examiner: Unknown

§ Atty. Docket: 2001-0128.00

JC971 U.S. PTO
09/945084
08/30/01

#4 / IDS
8-21-02
Rader

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

"EXPRESS MAIL" MAILING LABEL NUMBER	EL672722976US
DATE OF DEPOSIT	August 30, 2001
I HEREBY CERTIFY THAT THIS PAPER IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL" POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 C.F.R. § 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO THE COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.	
<i>Peggy Lloyd-Juster</i> Signature	

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant respectfully requests that this Information Disclosure Statement be entered and that the references listed on the attached Form PTO-1449 be considered by the Examiner and made of record. A copy of the listed reference is enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R. § 1.97(b), this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possible material information as defined in 37 C.F.R. § 1.56(a) exists.

The following references are submitted for the Examiner's review:

U.S. Patents

<u>U.S. Patent No.</u>	<u>Issue Date</u>	<u>Inventor</u>
6,169,329	01/01	Farnworth et al.
6,097,098	08/00	Ball
6,078,100	06/00	Duesman et al.
6,087,707	07/00	Lee et al.
5,110,754	05/92	Lowrey et al.
5,241,496	08/93	Lowrey et al.
5,324,681	06/94	Lowrey et al.

5,331,196	07/94	Lowrey et al.
6,094,388	07/00	Cowles
6,088,282	07/00	Loughmiller et al.
5,345,110	09/94	Renfro et al.
6,108,260	08/00	Casper
6,154,851	11/00	Sher et al.

Related Art

Synchronous DRAM 64Mb: x4, x8, x16 SDRAM, Micron Technology, Inc., p. 65-Rev D; Pub. 5/01

Synchronous DRAM 64Mb: x32 SDRAM, Micron Technology, Inc., p. 65-Rev. 9/00

Synchronous DRAM 128Mb: x4, x8, x16 SDRAM, Micron Technology, Inc., p. 65-Rev. E; Pub. 5/01

As this information is being submitted before a first office action on the merits, Applicant understands that no fee or certification is required for the submission and consideration of this information at this time.

If there are any matters which may be resolved or clarified through telephone interview, the Examiner is respectfully requested to contact Applicant's undersigned attorney at the number indicated.

* * * *

A Form PTO-1449 is enclosed herewith.

Respectfully submitted,

Date: 30-Aug-01

Kevin D. Martin
 Reg. No. 37,882
 Micron Technology, Inc.
 8000 S. Federal Way
 Boise, ID 83716-9632
 (208) 368-4516
 AGENT FOR APPLICANT